	Application No.	Applicant(s)
	10/738,496	DJOMEHRI ET AL.
Notice of Allowability	Examiner	Art Unit
	Hung Vu	2811
The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI	(OR REMAINS) CLOSED or other appropriate comm GHTS. This application is	in this application. If not included nunication will be mailed in due course. THIS
1. This communication is responsive to <u>12/14/04</u> .		
2. The allowed claim(s) is/are <u>9-12</u> .		
3. \boxtimes The drawings filed on <u>17 December 2003</u> are accepted by	the Examiner.	
 4. Acknowledgment is made of a claim for foreign priority ur a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority do International Bureau (PCT Rule 17.2(a)). * Certified copies not received: 	been received. been received in Applicat	ion No
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		le a reply complying with the requirements
5. A SUBSTITUTE OATH OR DECLARATION must be subminformal PATENT APPLICATION (PTO-152) which give		
6. CORRECTED DRAWINGS (as "replacement sheets") mus	st be submitted.	
(a) I including changes required by the Notice of Draftspers	-	ew (PTO-948) attached
1) hereto or 2) to Paper No./Mail Date		to the Office action of
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	s Amendment / Comment o	or in the Office action of
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t	.84(c)) should be written on he header according to 37 C	the drawings in the front (not the back) of FR 1.121(d).
7. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT		
Attachment(s)		
1. Notice of References Cited (PTO-892)	5. Notice of I	nformal Patent Application (PTO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)		Summary (PTO-413), ./Mail Date
3. Information Disclosure Statements (PTO-1449 or PTO/SB/C Paper No./Mail Date	08), 7. ⊠ Examiner'	s Amendment/Comment
Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. ⊠ Examiner' 9. ☐ Other	-Hung Un
		Hung Vu Primary Examiner

Examiner's Amendment

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Ronald Coslick, on 03/04/05.

The application has been amended as follows:

- a. Cancel non-elected claims 1-8 and 13-22.
- b. In claim 12, line 1, changed "a metal layer" to "the metal layer".

Allowable Subject Matter

2. The following is an examiner's statement of reasons for allowance:

Applicant's claims 9-12 are allowable over the references of record because none of these references disclose or can be combined to yield the claimed method of forming a semiconductor device comprising forming an NMOS transistor and a PMOS transistor having respective gates each comprising a polysilicon portion and a silicide portion, channel regions of the NMOS transistor and the PMOS transistor comprise strained silicon, forming a metal layer in contact with the silicide portion of the NMOS gate while protecting the PMOS gate, and annealing to fully silicide the NMOS gate.

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Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Wu (PN 5,920,774), Liang et al. (PN 6,130,123), Bai et al. (PN 6,204,103) and Lin et al. (PN 6,750,519) disclose the method of forming the CMOS transistors.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hung K. Vu whose telephone number is (571) 272-1666. The examiner can normally be reached on Mon-Thurs 6:00-3:30, alternate Friday 7:00-3:30, Eastern Time.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie C. Lee can be reached on (571) 272-1732. The Central Fax Number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

Vu

March 4, 2005

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Hung Vu

Primary Examiner